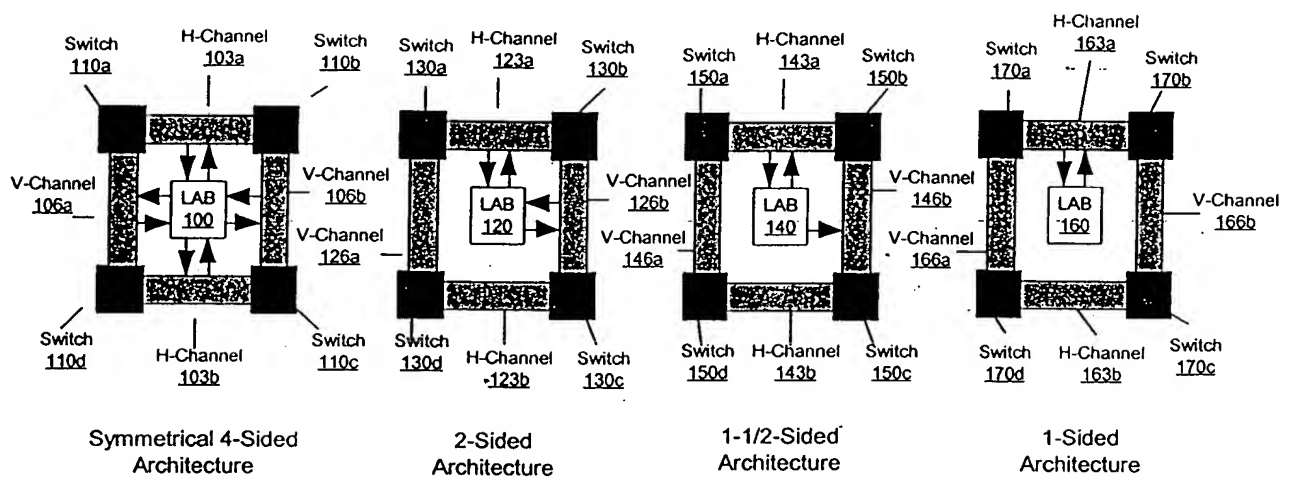


Fig. 1

Prior Art Routing Architectures



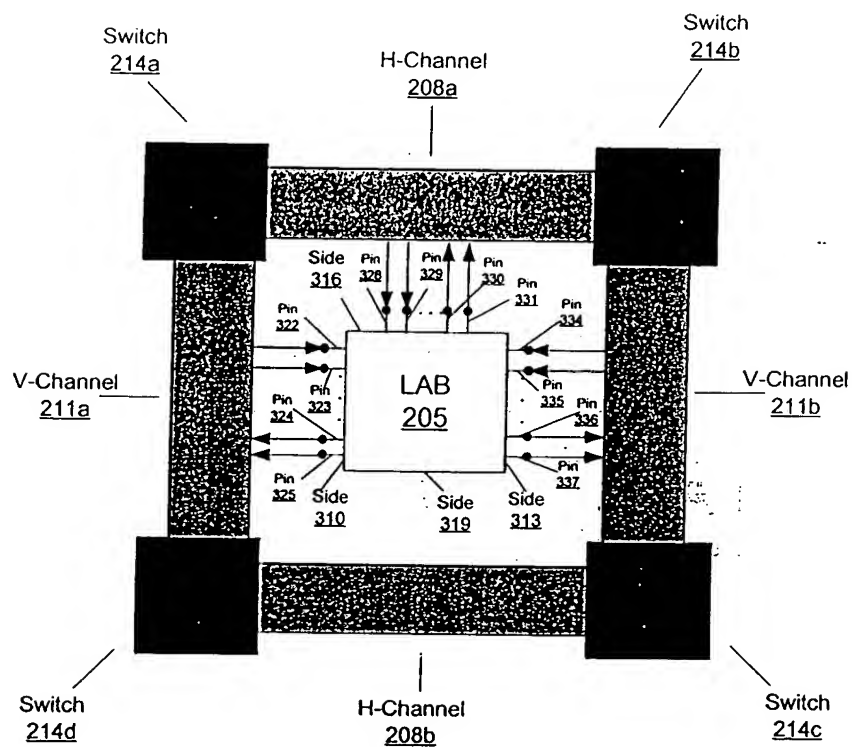
Switch



Channel (e.g., Horizontal or Vertical)



LAB



3-Sided Architecture

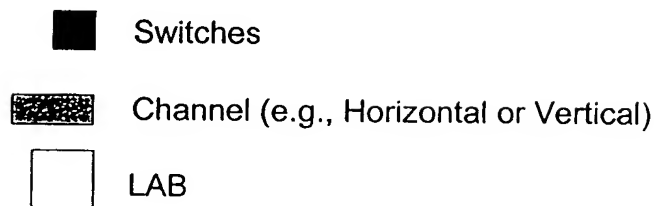
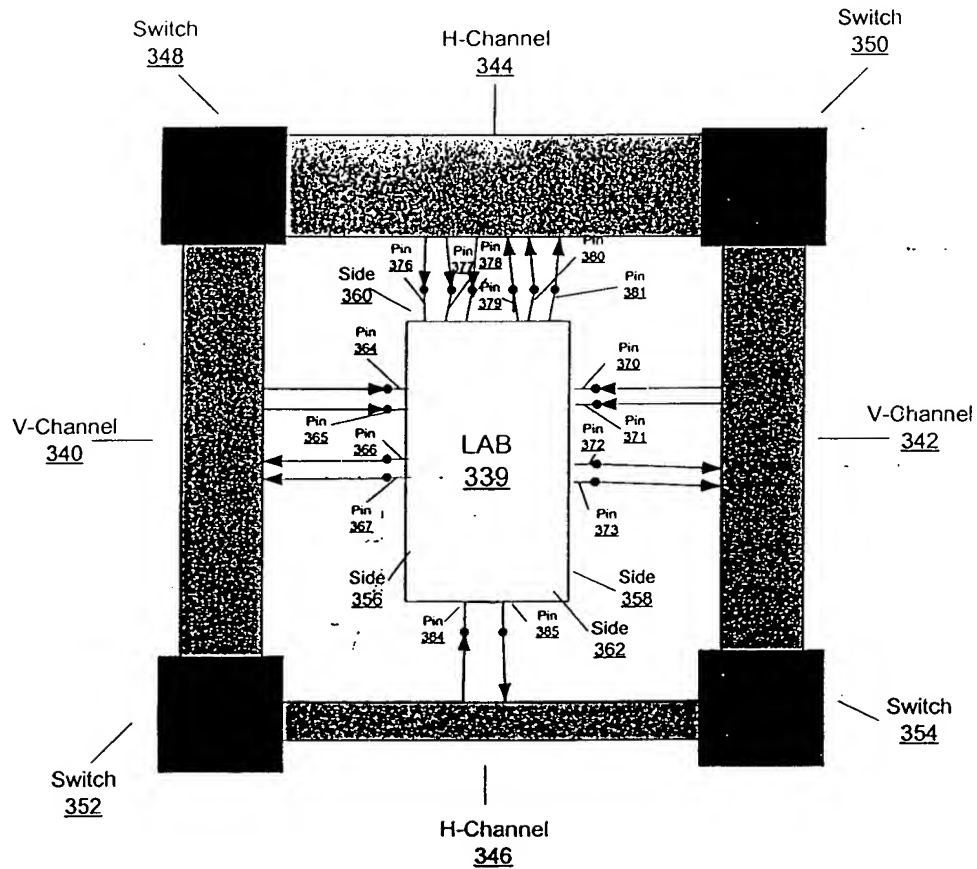


Fig. 2b



Asymmetrical 4-Sided Architecture

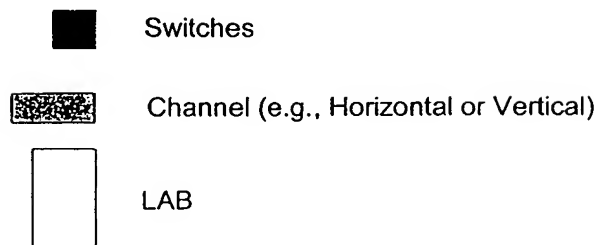


Fig. 3

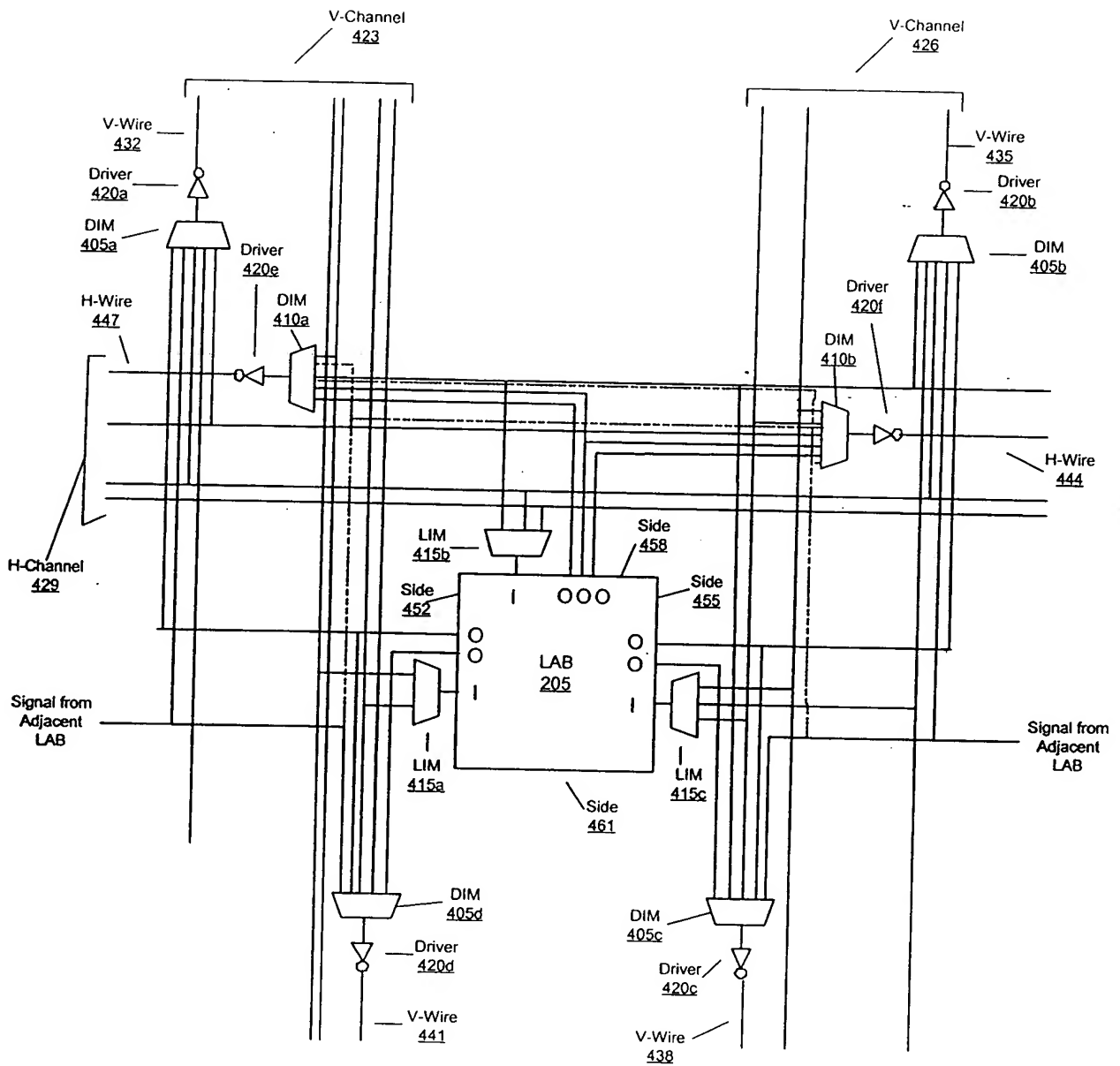


Fig. 4

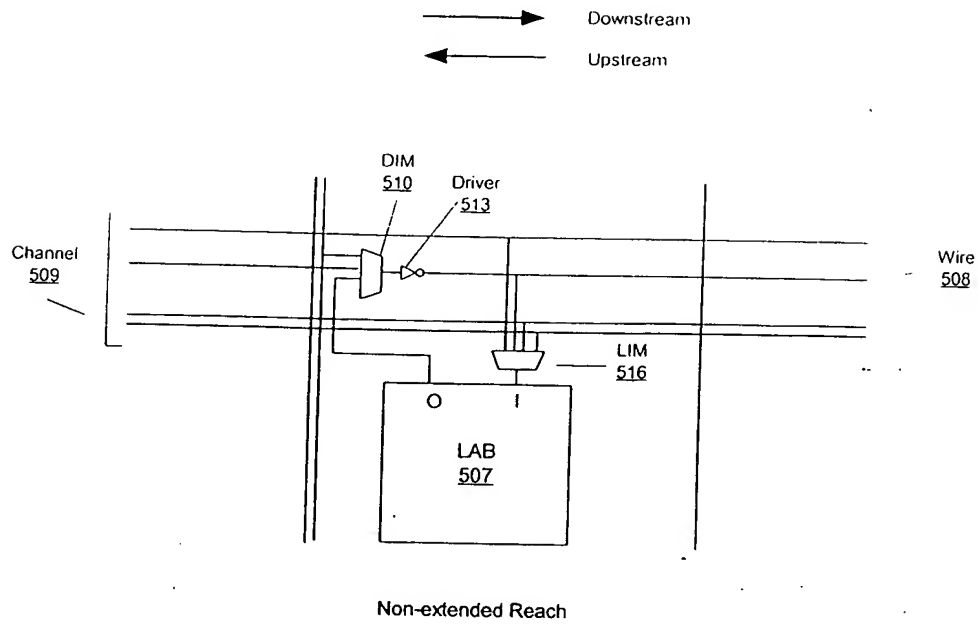


Fig. 5

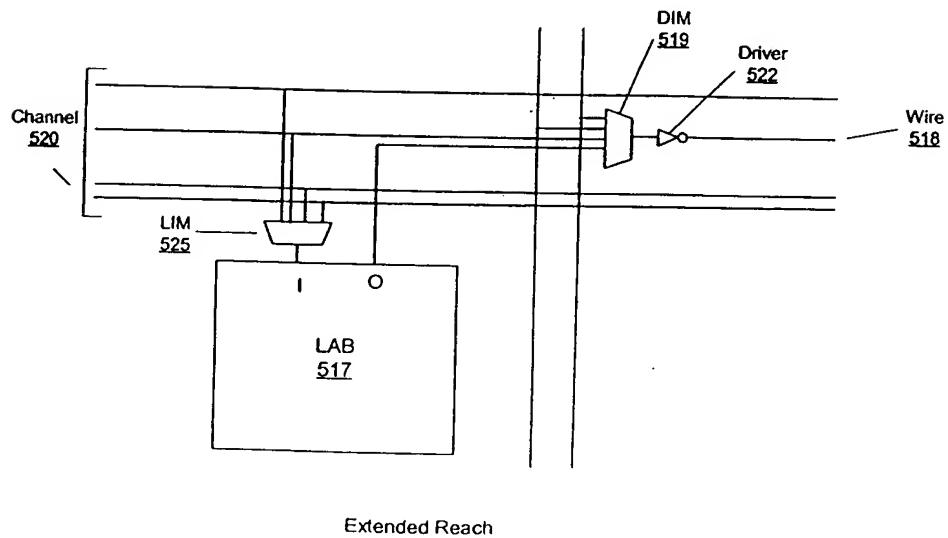


Fig. 6

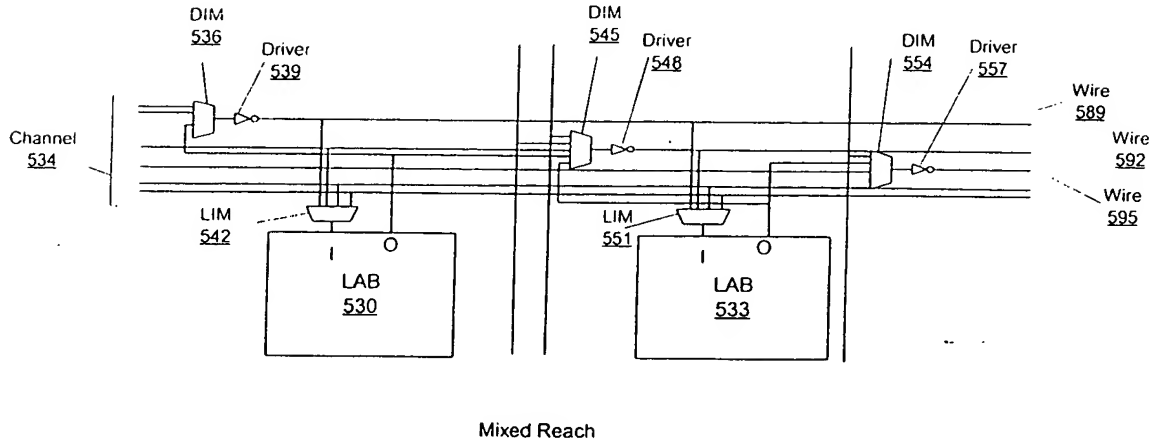


Fig. 7

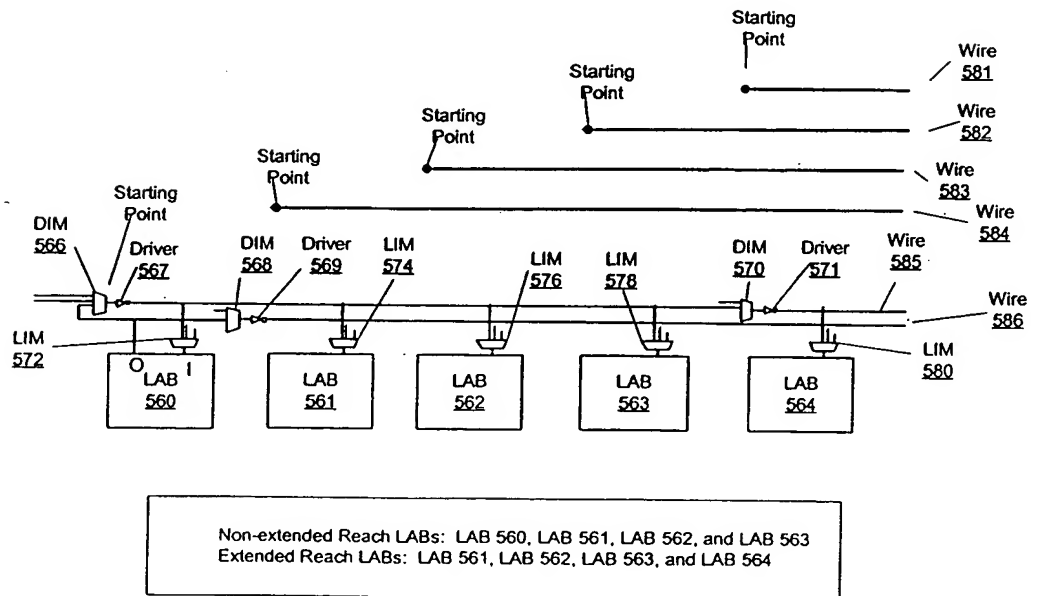


Fig. 8

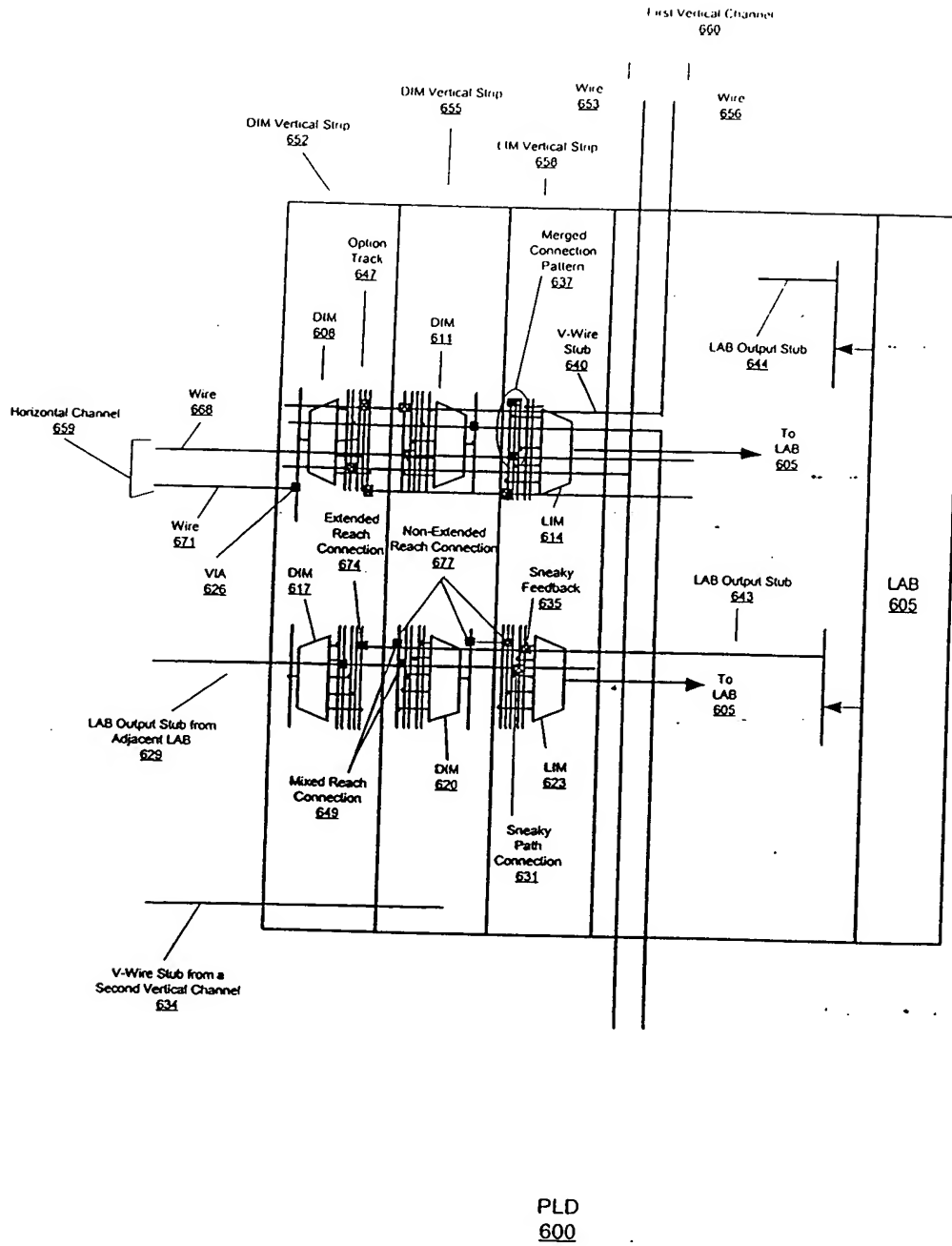


Fig. 9

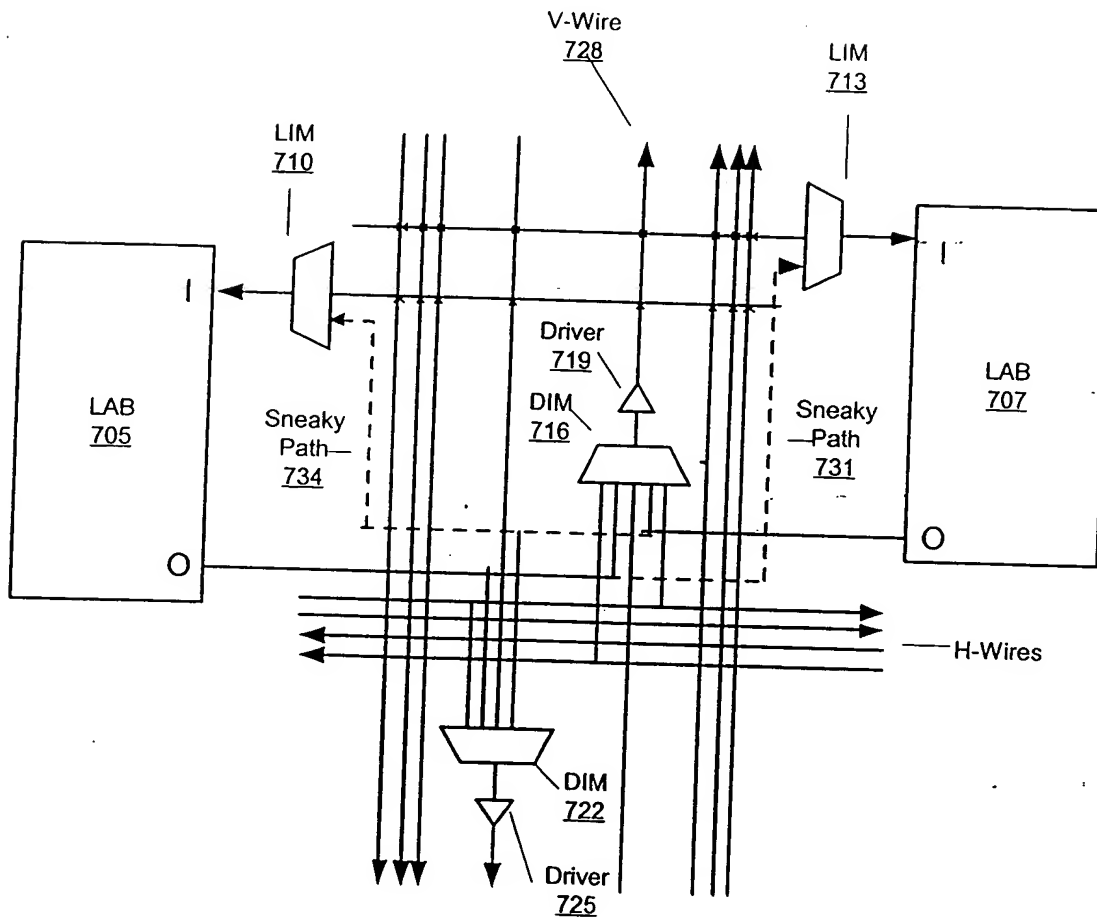
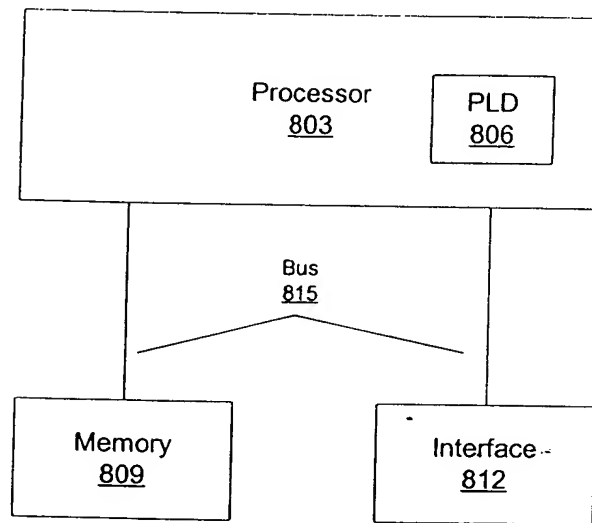


Fig. 10



Electronic System
801